

Abstracts

Quantitative extraction and compensation of excess inductance: case study of a digital attenuator chip

S.C. Choi, D.H. Kwon, K.H. Kim and S.W. Hwang. "Quantitative extraction and compensation of excess inductance: case study of a digital attenuator chip." 2002 MTT-S International Microwave Symposium Digest 02.3 (2002 Vol. III [MWSYM]): 2249-2252 vol.3.

The excess inductance of the conductor connecting the ground pins of the MMIC and the ground plane is extracted from a simplified procedure of measurement and optimization. The method is applied to a digital attenuator chip with a positive bias circuit. The addition of bypass capacitors with the value compensating the extracted excess inductance of the conductor clearly improves the performance of the chip.

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